

Electronic Patent Application Fee Transmittal

Application Number:	10814968			
Filing Date:	31-Mar-2004			
Title of Invention:	Integrated circuit having a memory cell array capable of simultaneously performing a data read operation and a data write operation			
First Named Inventor/Applicant Name:	Kyo-Min Sohn			
Filer:	Frank Chau/Richard D. Ratchford, Jr.			
Attorney Docket Number:	8021-225 (SS-18860-US)			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				
Extension - 1 month with \$0 paid	1251	1	120	120

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				120